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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/756,814	01/13/2004	James J. Spilker JR.	RSM051001	1515
	7590 03/18/200 OF RICHARD A. DU	EXAMINER		
343 SOQUEL AVENUE			FOTAKIS, ARISTOCRATIS	
SUITE 311 SANTA CRUZ	, CA 95062	ART UNIT	PAPER NUMBER	
			2611	
			MAIL DATE	DELIVERY MODE
			03/18/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Advisory Action Before the Filing of an Appeal Brief

Application No.		Applicant(s)	
	10/756,814	SPILKER, JAMES J.	
	Examiner	Art Unit	
	ARISTOCRATIS FOTAKIS	2611	

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The MAILING DATE of this communication appe	ars on the cover sheet with the c	orrespondence add	ress
THE REPLY FILED 28 February 2008 FAILS TO PLACE THIS	APPLICATION IN CONDITION FO	R ALLOWANCE.	
1. The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following application in condition for allowance; (2) a Notice of Appelor Continued Examination (RCE) in compliance with 37 Coperiods:	replies: (1) an amendment, affidavit eal (with appeal fee) in compliance v	, or other evidence, w with 37 CFR 41.31; or	hich places the (3) a Request
a) The period for reply expires 5 months from the mailing date b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire to Examiner Note: If box 1 is checked, check either box (a) or (MONTHS OF THE FINAL REJECTION. See MPEP 706.07(the	dvisory Action, or (2) the date set forth i ater than SIX MONTHS from the mailing b). ONLY CHECK BOX (b) WHEN THE	date of the final rejection	n.
Extensions of time may be obtained under 37 CFR 1.136(a). The date have been filed is the date for purposes of determining the period of extunder 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL	ension and the corresponding amount on thortened statutory period for reply original than three months after the mailing date	of the fee. The appropria nally set in the final Office	te extension fee e action; or (2) as
2. The Notice of Appeal was filed on A brief in comp filing the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed with AMENDMENTS	nsion thereof (37 CFR 41.37(e)), to	avoid dismissal of the	
3. The proposed amendment(s) filed after a final rejection, to (a) They raise new issues that would require further core (b) They raise the issue of new matter (see NOTE below (c) They are not deemed to place the application in beta appeal; and/or (d) They present additional claims without canceling a content of the con	nsideration and/or search (see NOT w); ter form for appeal by materially rec	E below); lucing or simplifying th	
NOTE: (See 37 CFR 1.116 and 41.33(a)). 4. The amendments are not in compliance with 37 CFR 1.12 5. Applicant's reply has overcome the following rejection(s): 6. Newly proposed or amended claim(s) would be all			
non-allowable claim(s). 7. For purposes of appeal, the proposed amendment(s): a) [how the new or amended claims would be rejected is prov The status of the claim(s) is (or will be) as follows: Claim(s) allowed: Claim(s) objected to: Claim(s) rejected: Claim(s) withdrawn from consideration: AFFIDAVIT OR OTHER EVIDENCE		be entered and an ex	xplanation of
 The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e). 			
 The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to o showing a good and sufficient reasons why it is necessary The affidavit or other evidence is entered. An explanation 	vercome <u>all</u> rejections under appea and was not earlier presented. Se	ll and/or appellant fails ee 37 CFR 41.33(d)(1)	s to provide a
REQUEST FOR RECONSIDERATION/OTHER 11. The request for reconsideration has been considered but		•	
See Continuation Sheet. 12. ☐ Note the attached Information Disclosure Statement(s). (13. ☐ Other:	PTO/SB/08) Paper No(s)		
/Chieh M. Fan/ Supervisory Patent Examiner, Art Unit 2611			

Continuation of 11. does NOT place the application in condition for allowance because:

Applicant respectfully submits that the description would enable one skilled in the art to make and use the claimed invention. Examiner submits that the disclosure of Applicants invention is inadequate. One skilled in the art would not have been able to use Applicants invention since there is no description on how to use the circuit of Fig.15 and make it compatible for ATSC signals as shown in Fig.18. More specifically, a designer would not know how to overcome the deficiencies that were presented by the Applicants prior art in Fig.15 and how would the invention as shown in figure 18 would overcome those deficiencies. Why would the circuitry of figure 15 would now work in Fig.18? The Applicant only discloses the names of the components listed in Fig.18 without providing any disclosure on how to generate the 10.76 MHz symbol clock.

Re claim 1, Applicant submits that none of the signals of Bradley have a pilot carrier. The Examiner submits that Bradley teaches of DQPSK and Scarpa teaches of QAM and VSB ATV signal where QAM, DQPSK, QPSK and VSB are well known television broadcast modulation methods. The claim does not recite of a pilot carrier and none of the recited components require the pilot carrier.

Applicant submits that Bradley does not appear to be capable of generating a symbol clock. The Examiner submits that discloses of a synchronization corrector (PLL, Col 4, Lines 28 - 30) that is capable generating a symbol clock.

Applicant submits that Bradley does not multiply the baseband signal and the delayed baseband signal but instead multiplies the baseband signal and a complex conjugate of the delayed baseband signal. Examiners submit that Bradley discloses the two components of Applicants claim. More specifically, a delay unit that delays the baseband signal and a multiplier that multiplies the baseband signal with the delayed baseband signal.

Applicant submits that Bradley does not show of the output of multiplier reaching the synchronization corrector but instead the output is used to generate metrics. Examiners submit that Bradley meets the recited limitation where it is shown of the synchronization corrector generating the symbol clock based on the output of the multiplier. The claimed limitation does not require of the output of multiplier reaching directly the synchronization corrector